

FIG.1

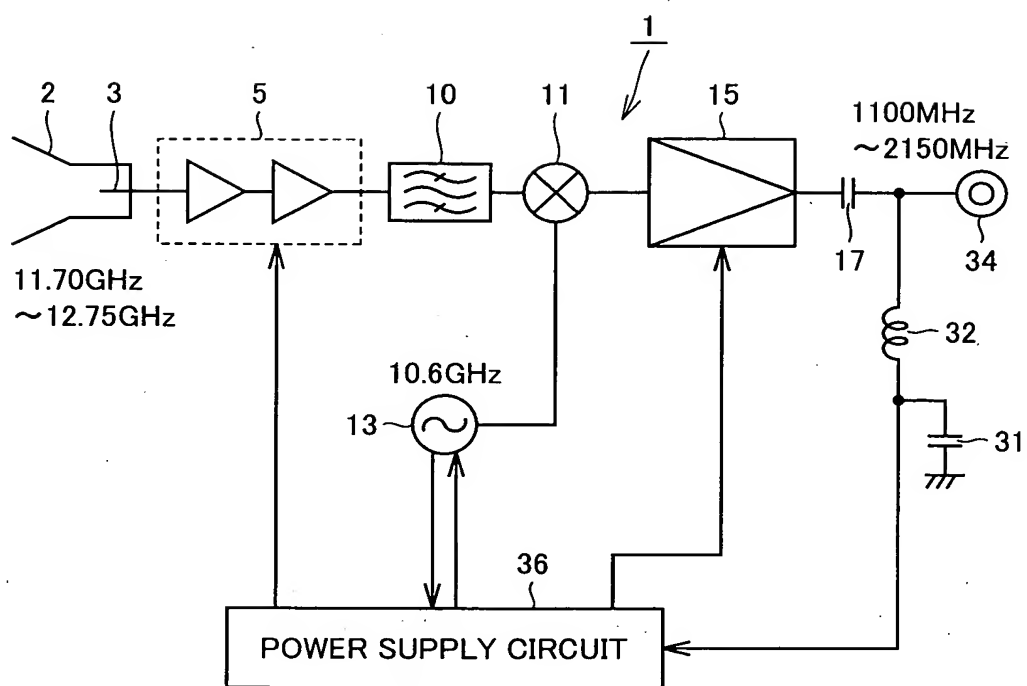


FIG.2

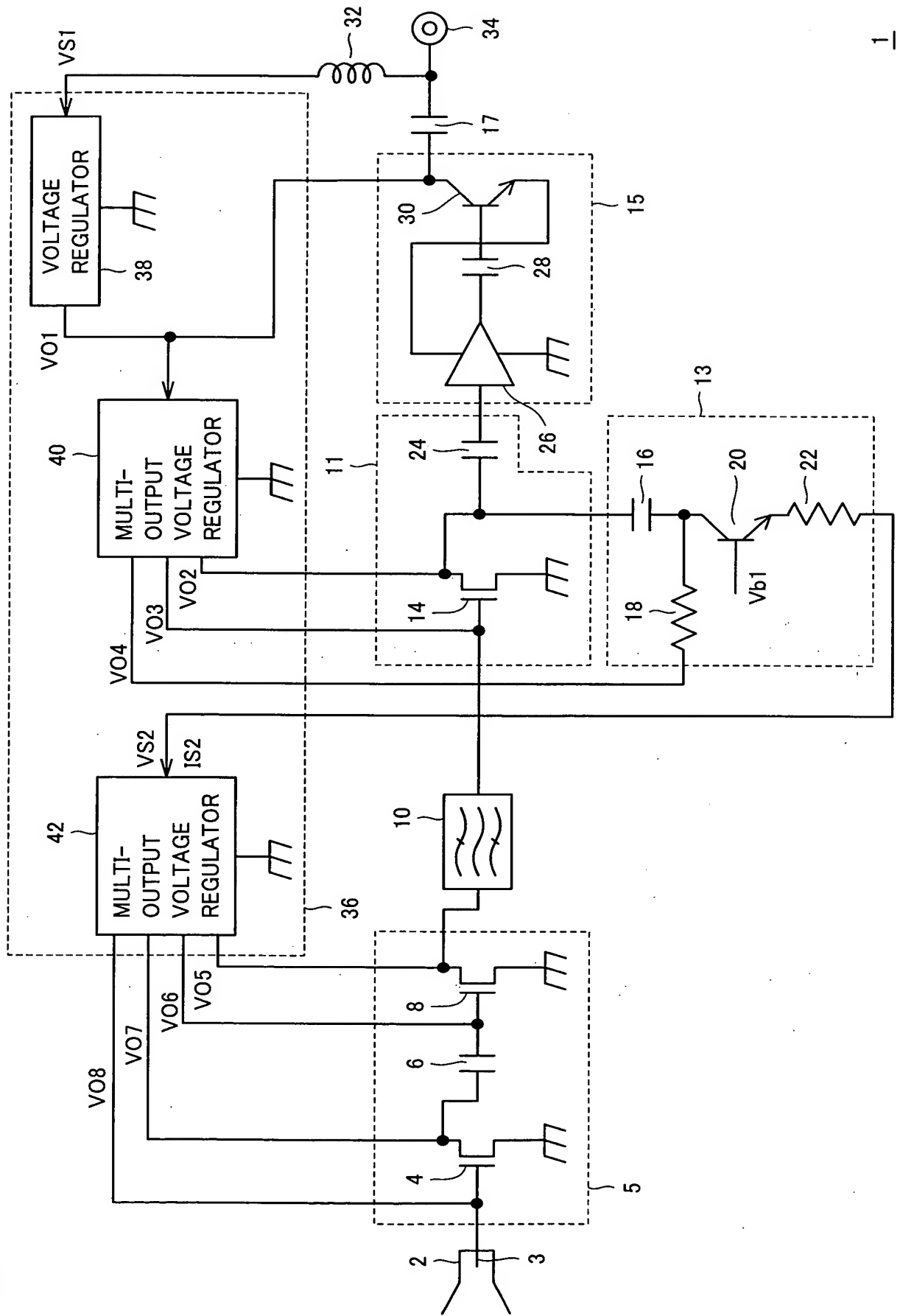


FIG.3

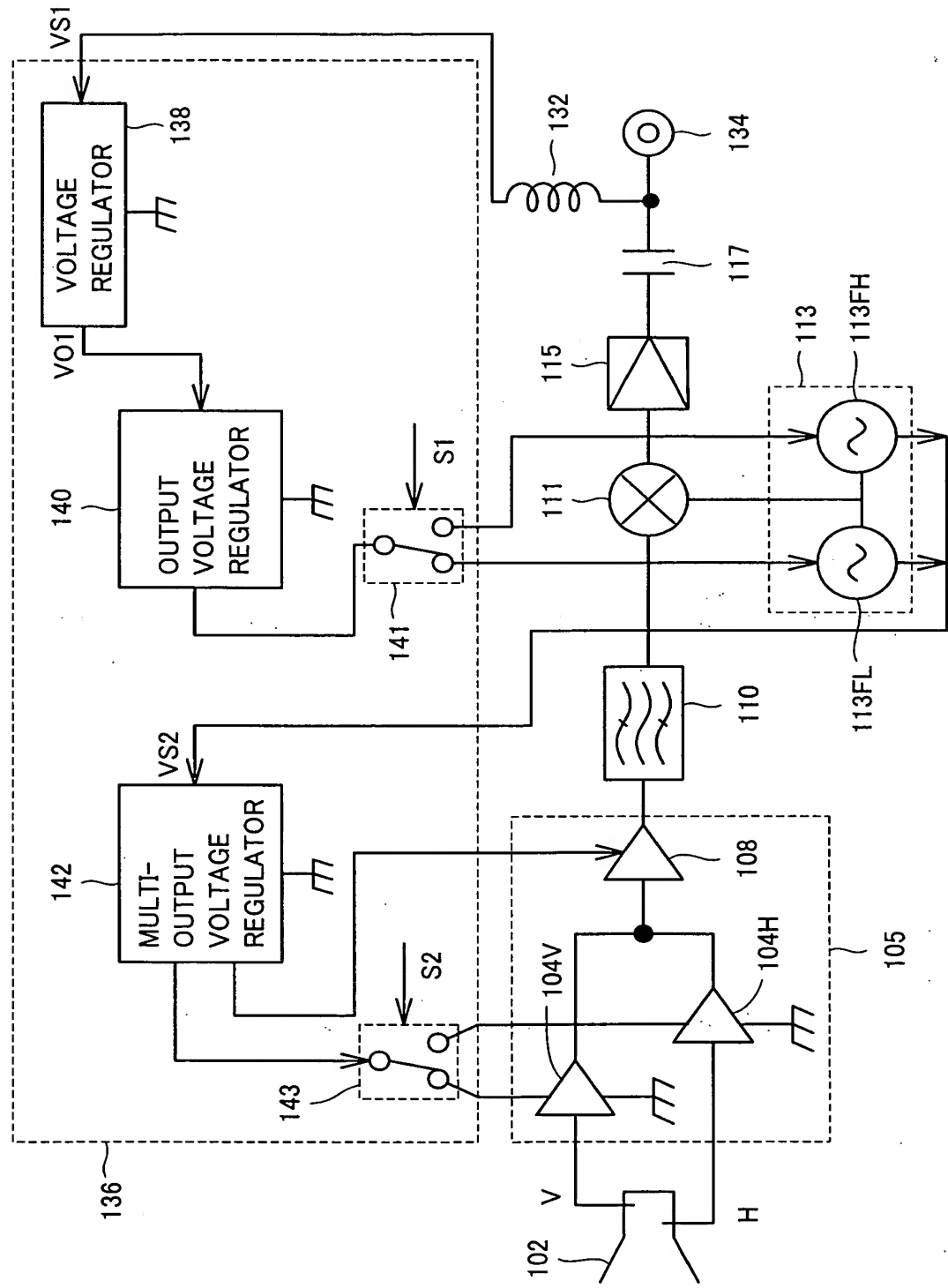
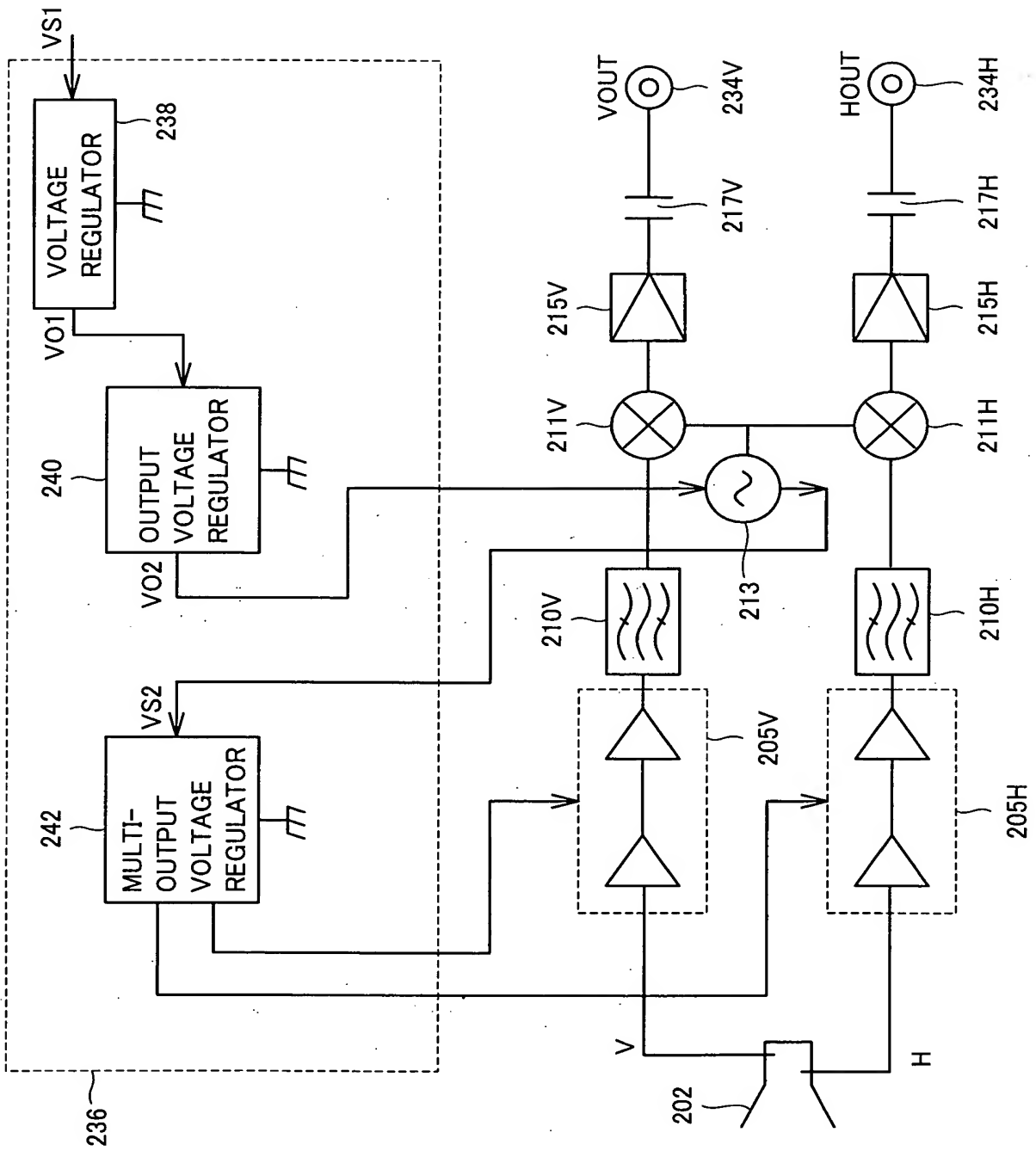


FIG. 4



The block diagram illustrates a power supply system 200 enclosed in a dashed box 236. It features three input voltage regulators at the top: a "VOLTAGE REGULATOR" 238 receiving input VS1 and outputting VO1; an "OUTPUT VOLTAGE REGULATOR" 240 receiving VO1 and outputting VO2; and a "MULTI-OUTPUT VOLTAGE REGULATOR" 242 receiving input VS2 and outputting multiple regulated voltages. The outputs from these regulators are connected to various components: VO2 goes to a transformer 210V and a bridge rectifier 205V; one output from 242 goes to a transformer 210H and a bridge rectifier 205H; another output from 242 goes to a switcher IC 214; and a third output from 242 goes to a common ground point labeled V. The switcher IC 214 also receives input from the multi-output regulator 242 and drives two switching transistors 215A and 215B through gate drivers 217A and 217B. These transistors control the primary windings of transformers 211V and 211H. Transformer 211V has a secondary winding connected to a full-wave bridge rectifier 213, which then connects to a filter capacitor 210V. Transformer 211H has a secondary winding connected to a full-wave bridge rectifier 213, which then connects to a filter capacitor 210H. Finally, the filtered DC outputs from both paths connect to a common output terminal H.

FIG. 6

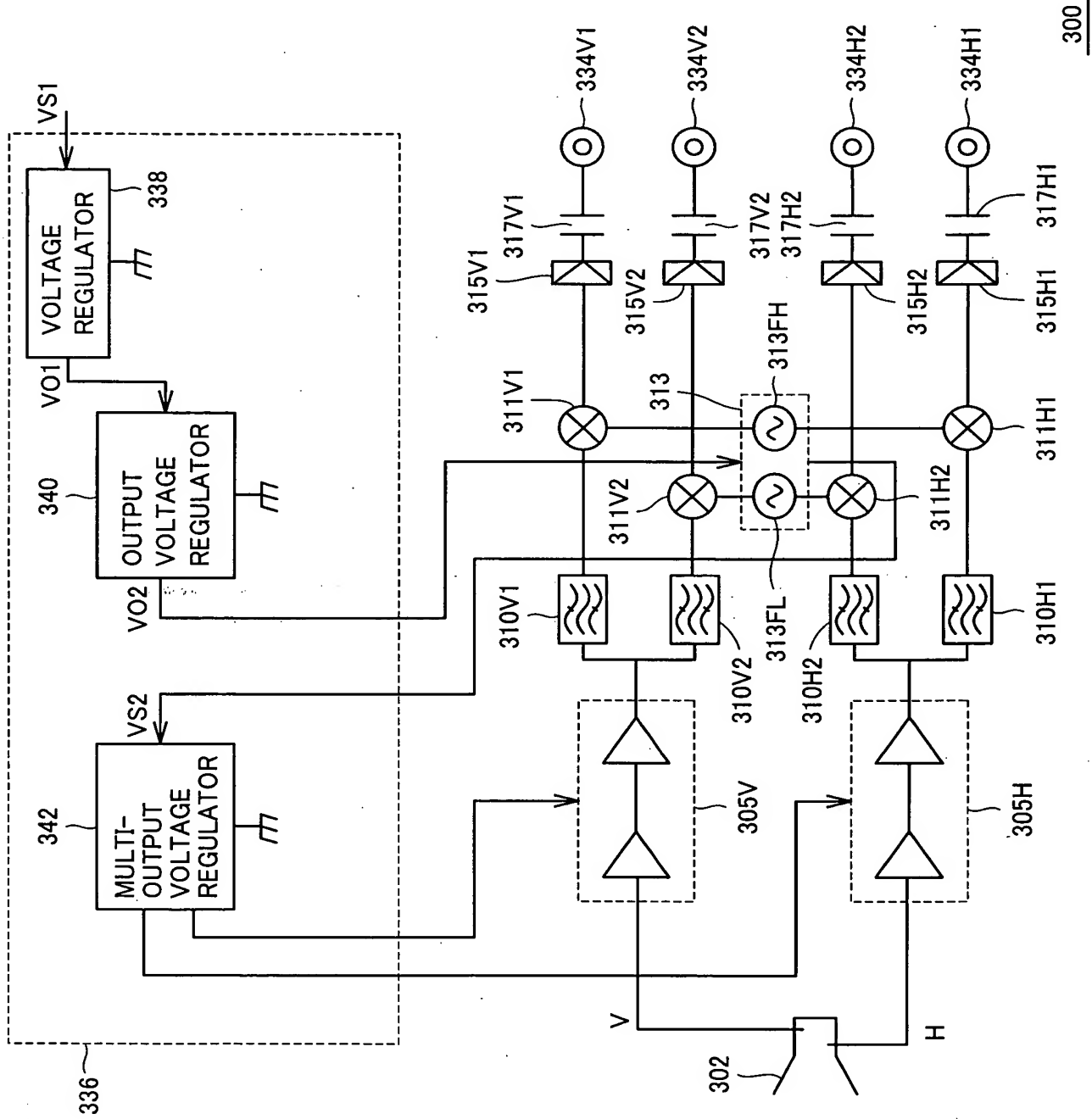
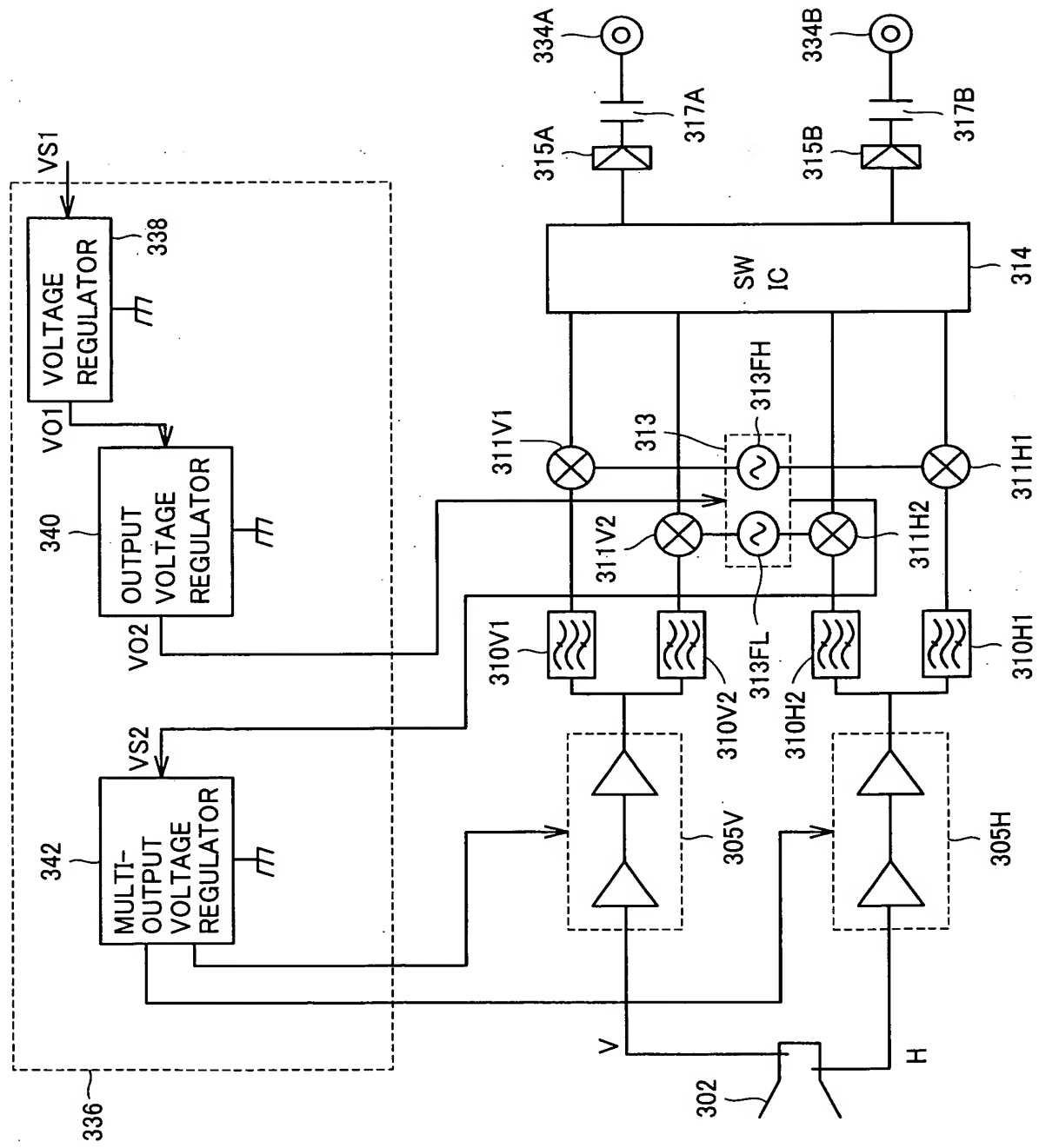


FIG. 7



[illegible]





FIG.10

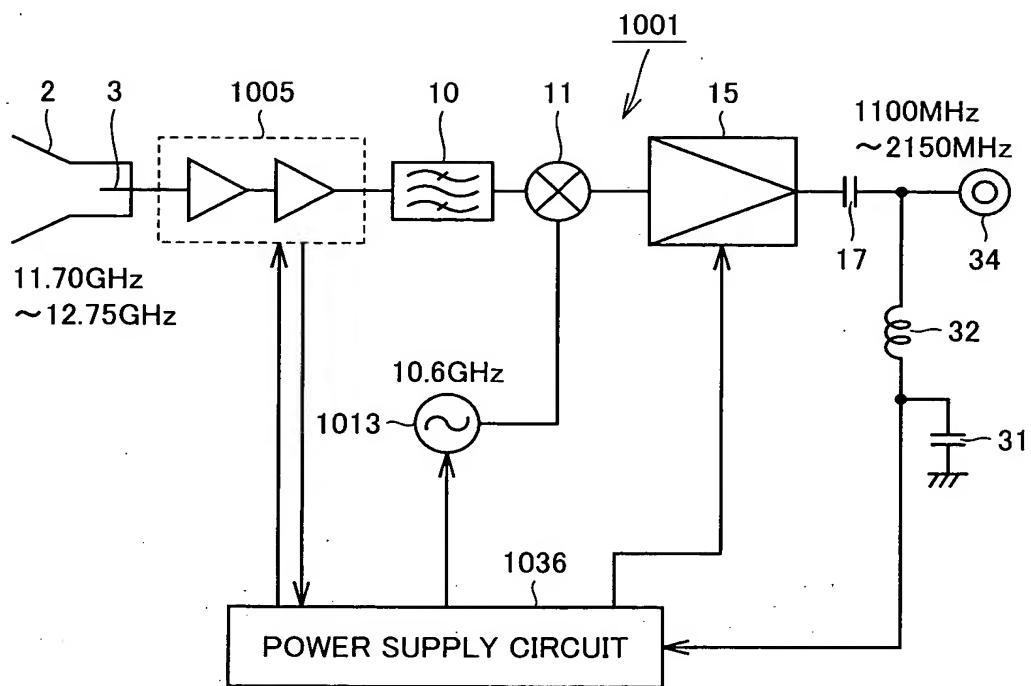
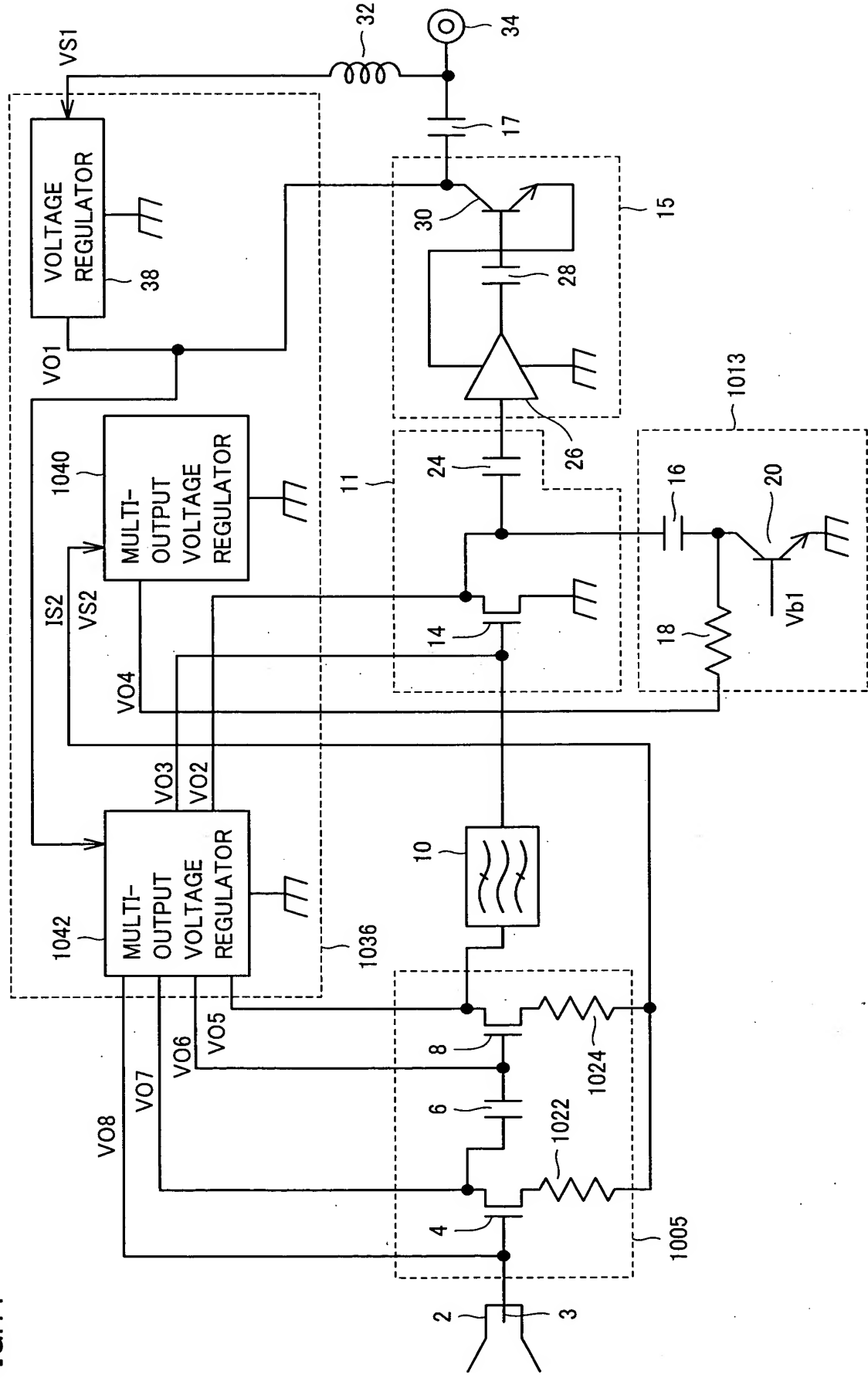


FIG.11



[illegible]

FIG.13

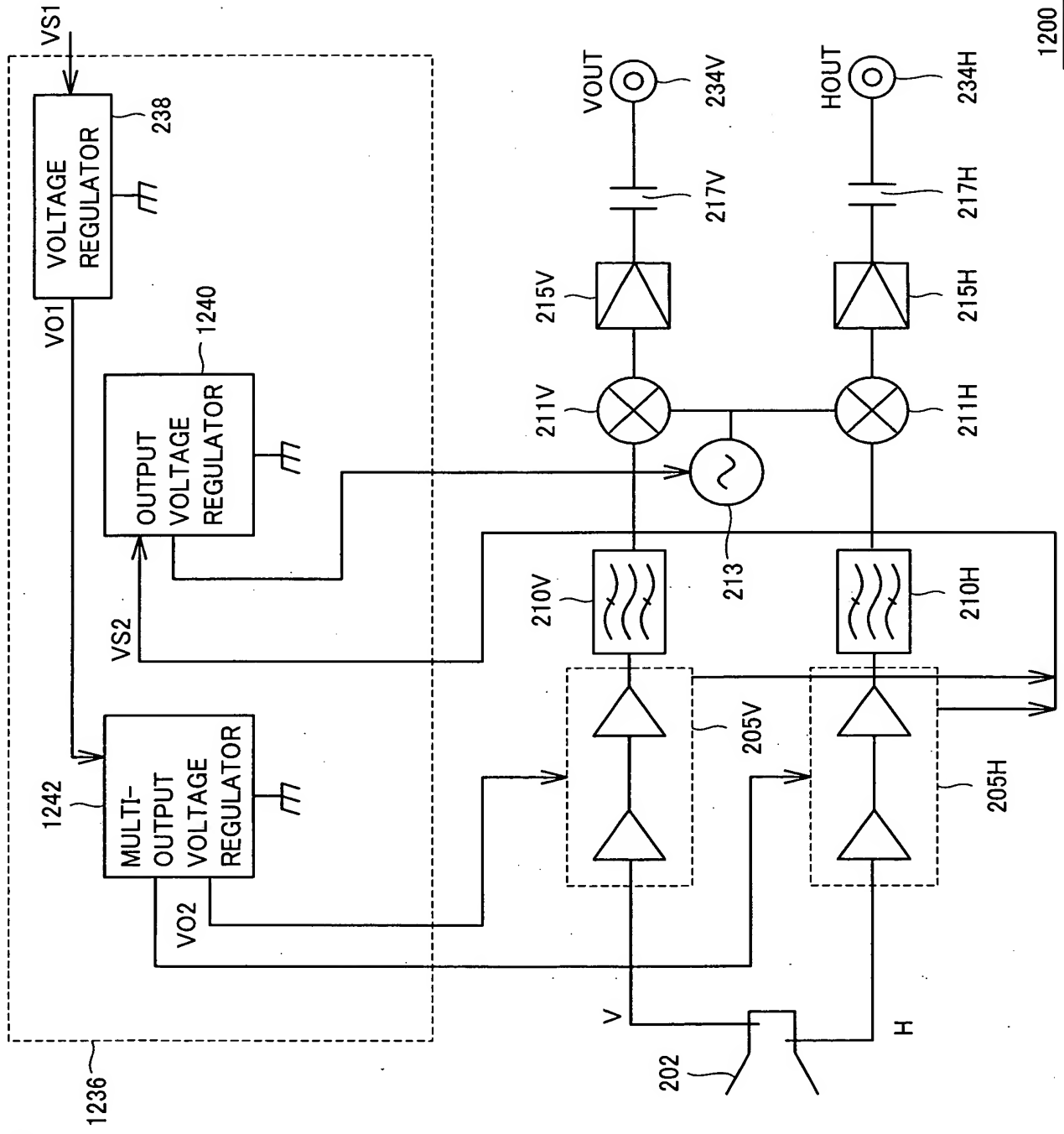


FIG.14

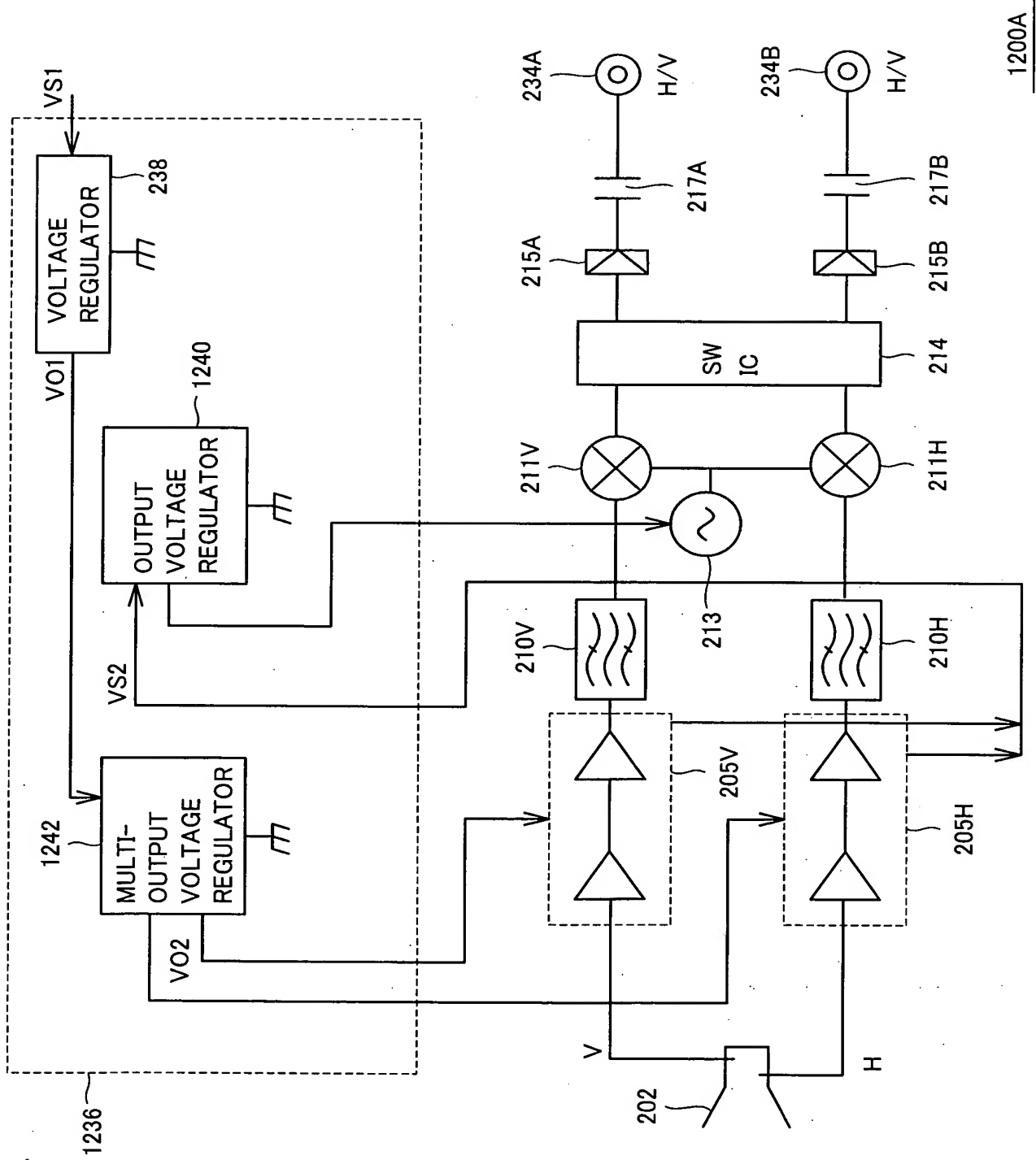
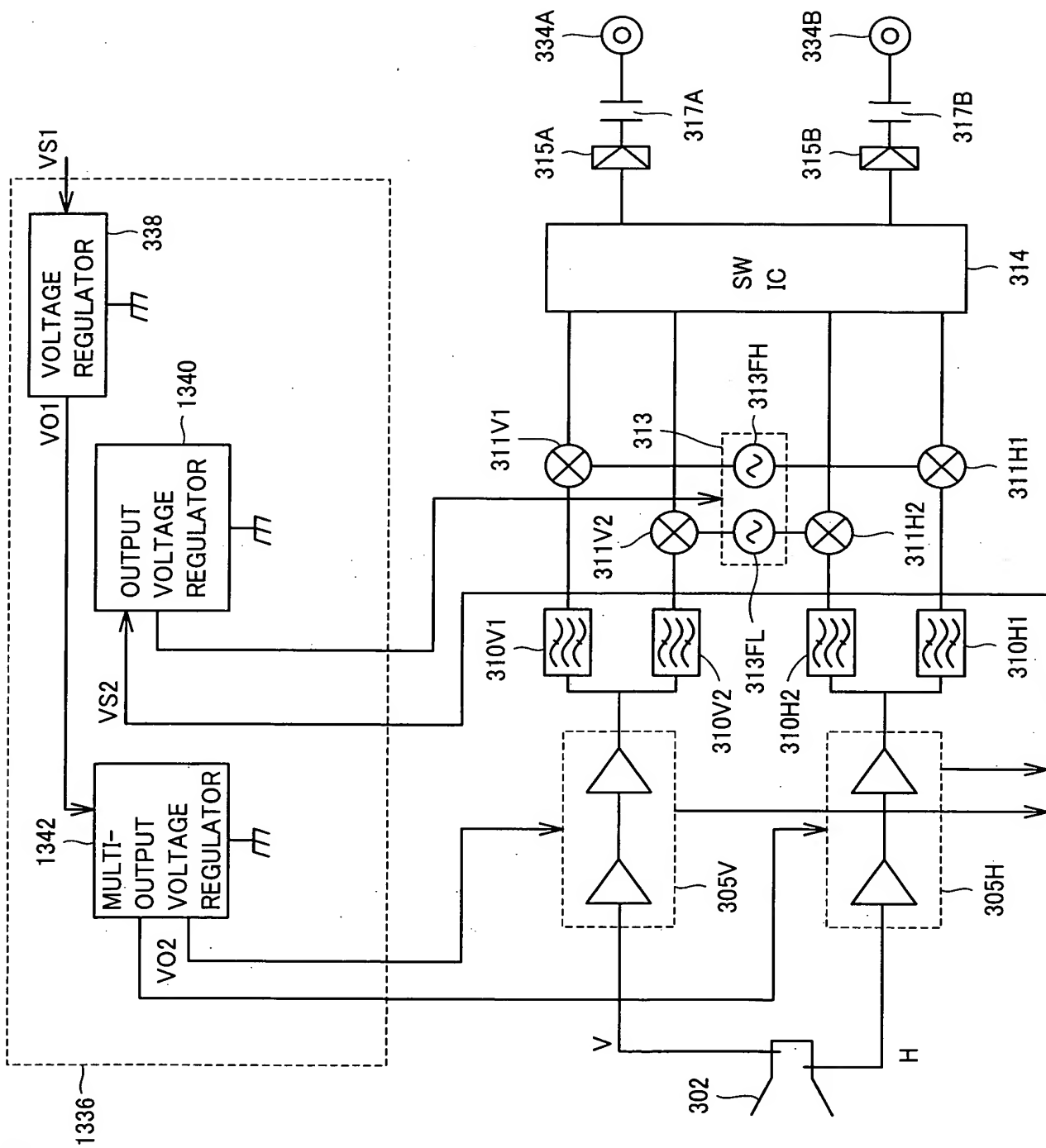




FIG.16





[illegible]

FIG.18

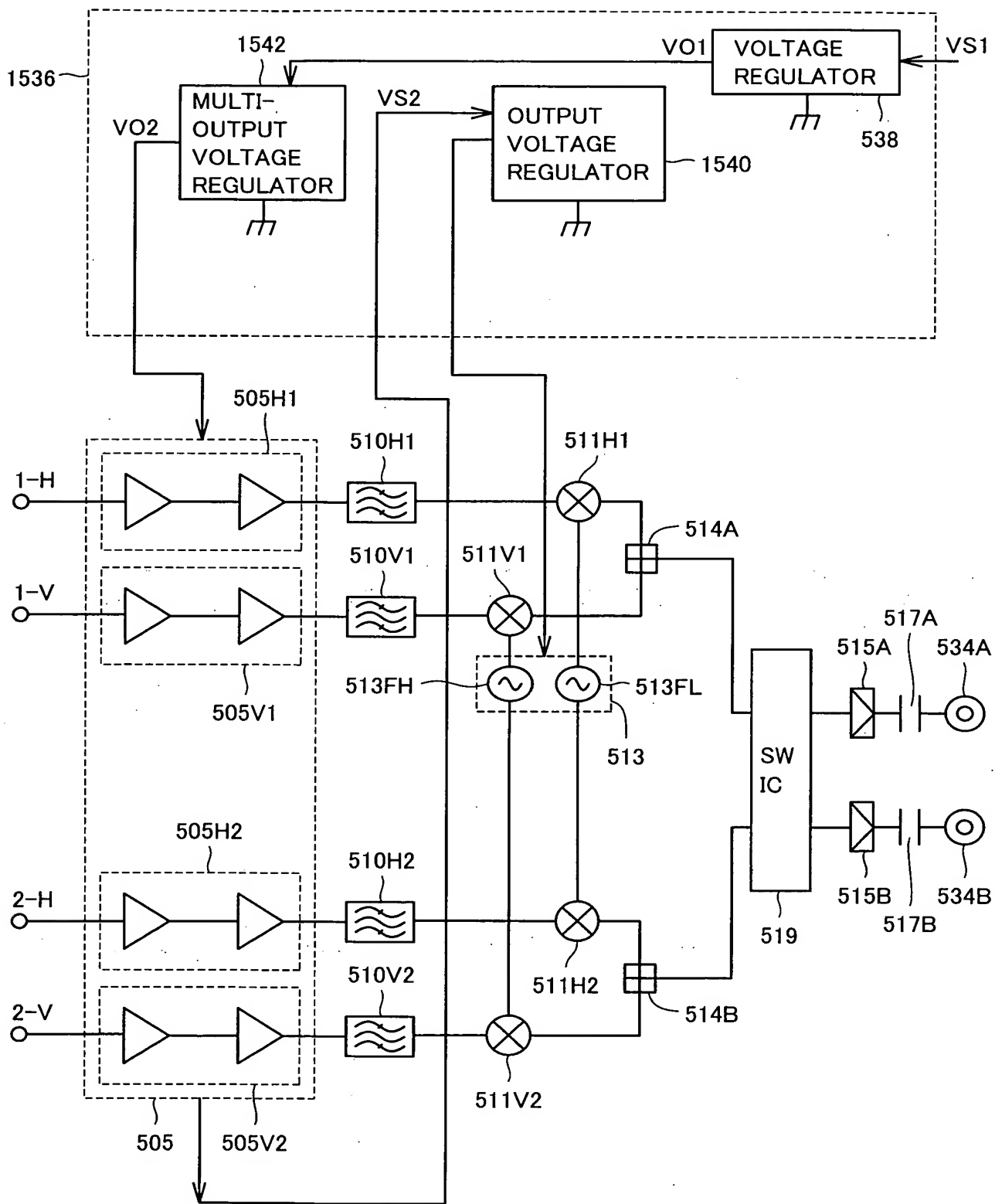


FIG.19 PRIOR ART

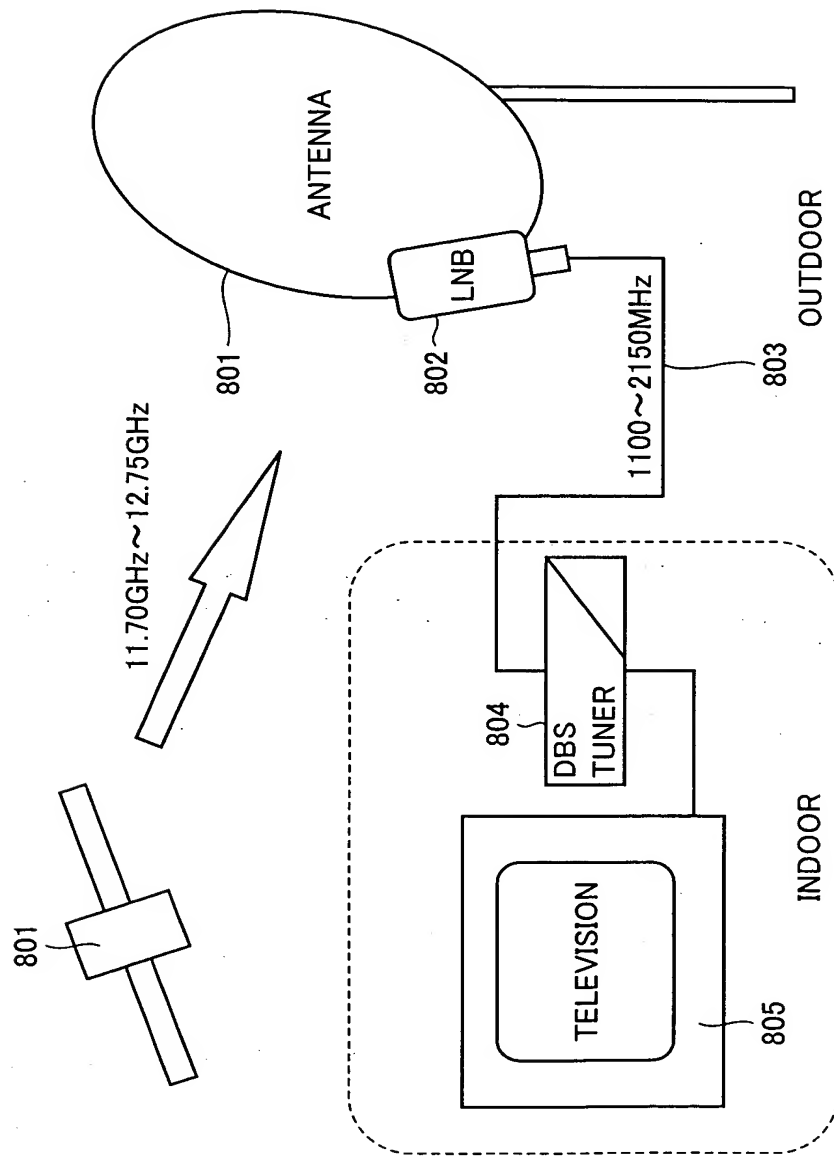


FIG.20 PRIOR ART

